

Curriculum Vitae Wolfgang Fichtner

Personal

Born February 11, 1951 in Oberzeiring, Austria
Married to Romy Fichtner
Two children, Johann (1979), Paul (1981)
Swiss Citizen. Bürger of Glarus and Netstal
Austrian Citizen

Education

Ph. D (cum laude) December 15, 1978
Technical University Vienna, Austria
M. S. (Physics) November 21, 1974
Technical University Vienna, Austria

ETH Website

www.iis.ee.ethz.ch/~fw

Employment

September 1985 - April 2011
Professor and Head
Integrated Systems Laboratory
Swiss Federal Institute of Technology (ETH) Zurich
Leave of absence (November 2004 - October 2007)

December 2006 – January 2008
General Manager, Silicon Engineering Group
Senior Vice President
Corporate Staff
Synopsys, Inc.
Mountain View, CA 94043

November 2004 – December 2006
General Manager, TCAD Business Unit
Vice President, Silicon Engineering Group
Synopsys, Inc.
Mountain View, CA 94043

October 1999 – October 2004
Department Chair
Department of Information Technology and Electrical Engineering
ETH Zurich, Switzerland

January 1996 – September 1997
Program Director
Swiss National Research Project MINAST

1993 – October 2004
Founder and CEO
ISE Integrated Systems Engineering AG
Zurich, Switzerland

March 1979 - August 1985
Member of Technical Staff, AT&T Bell Laboratories,
Supervisor, VLSI Device Analysis and Simulation Group (from August 1982)
Bell Laboratories, Murray Hill, NJ 07974, USA

Awards

ISPSD 2004 Contributory Award (2004)
IEEE Andrew S. Grove Field Award (2000)
2nd Prize Seymour Cray Competition (1990)
Denzler Prize for Excellent Research in Electrical Engineering (1992)

Publications

Author of more than 560 reviewed scientific papers and conference presentations
Editor of four books



Memberships

Member of the Swiss National Academy of Engineering
Corresponding Member of the Austrian Academy of Sciences

Fellow Institution of Electrical and Electronics Engineers (IEEE) - IEEE Electron Device and
IEEE Laser and Electro-Optics Society

American Astronomical Society (AAS)
American Physical Society (APS)
American Association for the Advancement of Science (AAAS)
Association for Computing Machinery (ACM)
Electrochemical Society (ECS)
Material Research Society (MRS)
Japanese Society of Applied Physics (JAP)
Society of Industrial and Applied Mathematics (SIAM)

Activities in Scientific and Engineering Organizations

Program Chairman First IEEE-SIAM Conference on Numerical Simulation of VLSI Devices
Boston, MA, November 2-4, 1982

Editor for VLSI Bell System Technical Journal 1982 – 1985

Guest Editor IEEE Transactions on Electron Devices September 1983

Guest Editor SIAM Journal on Scientific and Statistical Computing September 1983

Conference Chairman Second IEEE-SIAM Conference on Numerical Simulation of VLSI Devices
Boston, MA, November 12-14, 1984

Associate Editor IEEE Transactions on Computer Aided Design 1984 – 1990

Committee Member 1984 International Electron Devices Meeting (IEDM)
Subcommittee on Integrated Circuits

Panel Organizer 1984 International Electron Devices Meeting (IEDM)
Panel Discussion on Power Supply Standards for Future VLSI Circuits (5V ?)

Committee Member 1985 IEEE Meeting on Large Scale Computational Device Modeling

Guest Editor IEEE Transactions on Computer Aided Design October 1985

Guest Editor IEEE Transactions on Electron Devices October 1985

European Editor IEEE Transactions on Electron Devices 1986 – 1989

European Editor IEEE Transactions on Computer-Aided Design 1986 – 1990

Organizer IFIP WG 10.5 VLSI Summer School on VLSI Tools and Applications 1986

Program Committee Member ISSDM 1987 -1992

President Swiss UNIX Users Group (till 12/88)

Organizer 2nd Annual Davos Seminar on Computer Methods and Tools in VLSI 1989

Organizer 3rd Annual Davos Seminar on Computer Methods and Tools in VLSI 1990

Conference Co-chairman SISDEP 1991 - 1995

Program Committee Member Device Research Committee 1991

Program Committee Member ESSDERC 1992 - 1996

Advisory and Technical Program Committee Member ISPSD 1993 -2007

Advisory Committee Member IWCE-5 1997 Norte Dame, IN, USA

Advisory Committee Member IWCE-6 1998 Osaka, Japan

Member of the IEEE Field Awards Committee 2001 - 2004

Member of IEEE Adcom, EDS Fellow Committee and Ebers Award Committee 2000 - now